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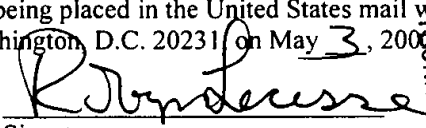
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Applicant: Ferguson et al.
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For: METHOD AND APPARATUS FOR USE IN
SWITCHED CAPACITOR SYSTEM
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Signature

Commissioner for Patents
Washington, D.C. 20231

Sir:

RESPONSE TO THE NOTICE OF NON-COMPLIANT AMENDMENT

(37 C.F.R. §1.121)

This paper responds to the Notice of Non-Compliant Amendment mailed on April 17, 2002 and is essentially identical to the amendment dated March 25, 2002, except that a clean version of claims 18 and 19, (omitted in error in the amendment of March 25, 2002) has been added.

✓ **In the Claims:**

Please amend the following claims:

1. (Amended) A system comprising:
a DAC that receives a multi-bit digital signal and outputs at least two analog signals including a first analog signal and a second analog signal, the first analog signal being indicative of a sum of values of bits in the multi-bit digital signal, the second analog signal also being indicative of said sum of values of said bits in the multi-bit digital signal.
2. (Amended) The system of claim 1 wherein the first analog signal and the second analog signal are substantially equal to each other.